Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-35 (cancelled).

36. (New) A semiconductor integrated circuit on a semiconductor chip, comprising:

a plurality of regulators located on the semiconductor chip, and generating internal power supply current;

a control circuit controlling operation of the plurality of regulators; and

an internal circuit which is provided said generated internal power supply current,

wherein said plurality of regulators are located in an I/O area, and

wherein said control circuit outputs a plurality of control signals to said plurality of regulators, which are controlled in operation according to said control signals.

37. (New) The semiconductor integrated circuit according to claim 36,

wherein each of said regulators is supplied with an external power voltage from outside of the semiconductor integrated circuit.

38. (New) The semiconductor integrated circuit according to claim 36,

wherein each said regulator includes a differential amplifier and a driver MOS transistor, and

wherein said differential amplifier receives one of said control signals, and is turned to either an on state or an off state according to that control signal.

39. (New) A semiconductor integrated circuit formed on a semiconductor chip, comprising:

a plurality regulating circuits providing internal supply current;

an internal circuit module provided with said internal supply current, and operating based on said internal supply current; and

a controlling module which controls on/off operation of said plurality of regulating circuits,

wherein said regulating circuits are located on an I/O area in which is located a plurality external terminals.

40. (New) The semiconductor integrated circuit according to claim 39,

wherein said controlling module is capable of outputting a plurality of control signals,

wherein said plurality of control signals are provided to said plurality regulating circuits, respectively, and

wherein each said regulating circuit operates according to the respective control signal provided thereto.

41. (New) The semiconductor integrated circuit according to claim 39,

wherein each said regulating circuit comprises an amplifier,

wherein said amplifier couples a MOS transistor which couples a path providing a power supply to said amplifier in series, and

wherein said MOS transistor is controlled in on/off operation by a control signal, provided by said controlling module to said amplifier.

42. (New) The semiconductor integrated circuit according to claim 41,

wherein said MOS transistor is coupled to an external power supply.